

Hardware Unit Test Report

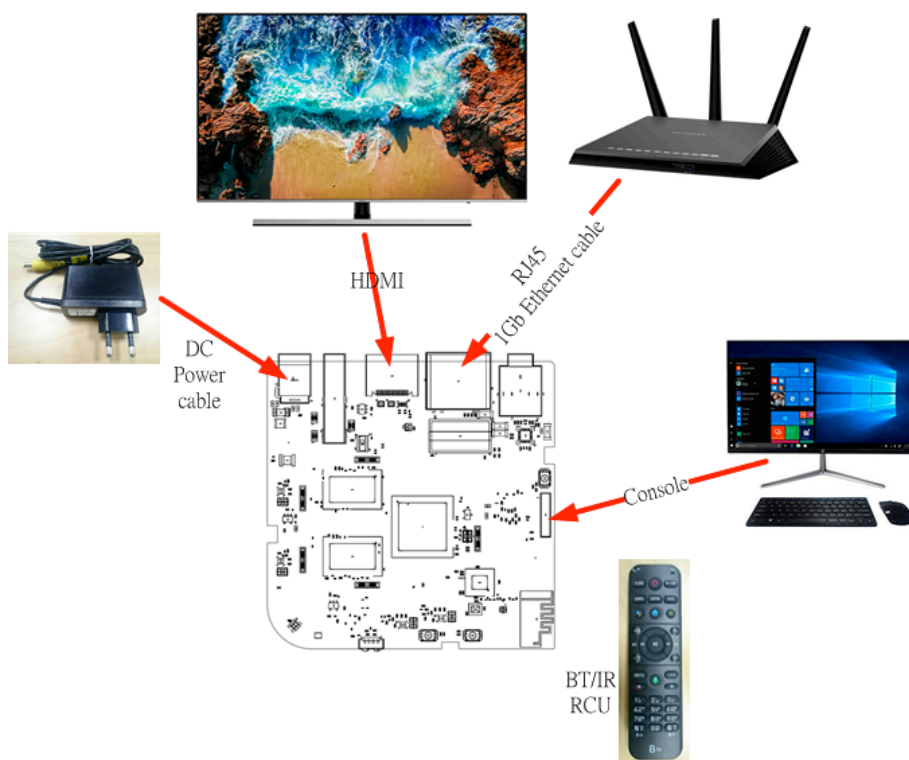
Set-Top Box

T99X171T00

Ethernet RGMII Timing Test and LED status

Hardware version	FW version	Sample No.	eMMC Flash P/N
V005	4.9.113 #1 Mon Feb 26 16:03:31 CST 2019 Build BFX_AT100-eng 9 PI 20190226 release-keys	S/N:	KLM8G1GETF-B041

1.1 Ethernet RGMII timing test structure



Note: Ethernet cable needs 1Gb for requirement.

Command:

MDC/MDIO: signal from MAC is keeping sending, no command is required.

RGMII RX/TX command:

iperf3 -c IP_address -t 3600

Note: use iperf Rx/Tx to measurement the signal.

1.2 Ethernet RGMII Specification

4.7.6 Ethernet Timing Specification

Figure 4-12. Management Data Timing Diagram

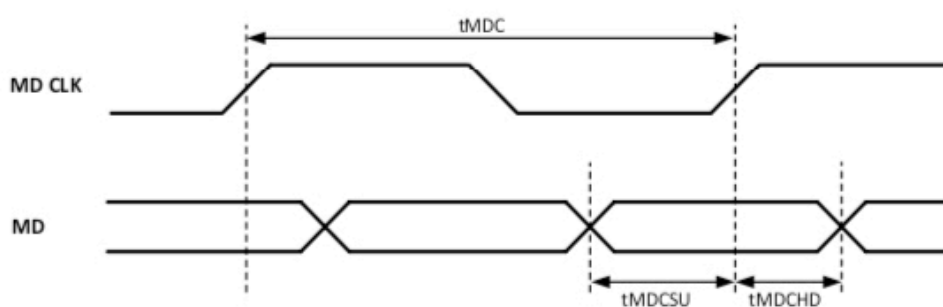


Table 4-10. Management Data Timing Specification

Symbol	Description	Min.	Typ.	Max.	Unit	Notes
tMDC	MDC clock Period	400	500		ns	From MAC
tMDCS U	Setup time to rising edge of MDC	10			ns	
tMDCH D	Hold time to rising edge of MDC	10			ns	

Figure 4-14. RGMII Receive Timing Diagram

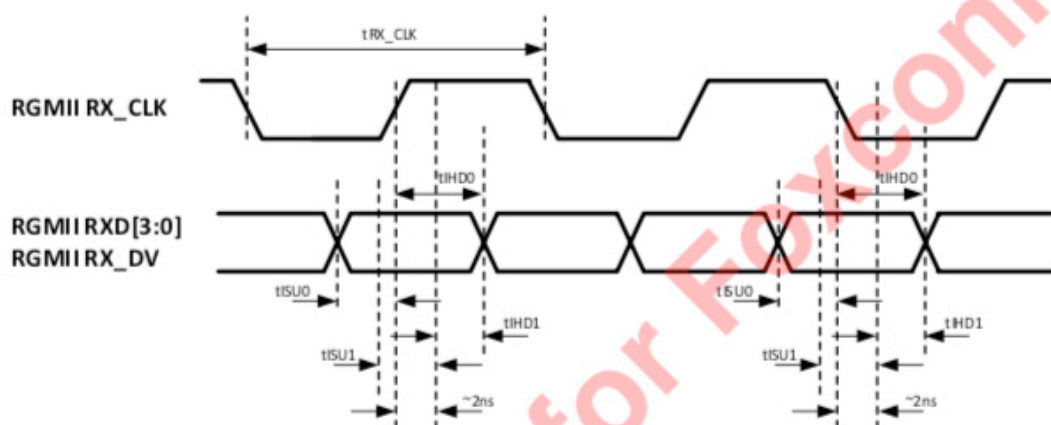


Table 4-12. RGMII Receive Timing Specification

Symbol	Description	Min.	Typ.	Max	Unit	Notes
t_{RX_CLK}	RGMII RX_CLK clock period		8		ns	125MHz from PHY
t_{SETUP}	RXD[3:0] & RX_DV setup time (PHY internal delay enabled)	1.2			ns	From PHY
t_{HOLD}	RXD[3:0] & RX_DV hold time (PHY internal delay enabled)	1.2			ns	From PHY
t_{SKEW}	RXD[3:0] & RX_DV skew between these 5 signals (PHY internal delay disabled)	-0.5		0.5	ns	From PHY

When PHY internal delay is enabled, check setup/hold timing.

When PHY internal delay is disabled, check signal skew.

Note: t_{SKEW} did not define in signal timing, so the measurement is RX_CLK vs RXD0 which is checked with Amlogic.

Figure 4-15. RGMII Transmit Timing Diagram

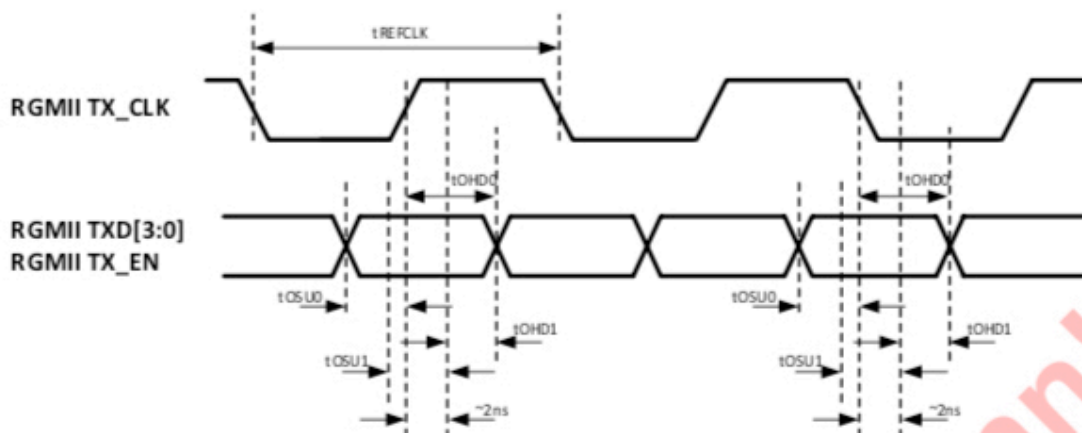


Table 4-13. RGMII Transmit Timing Specification

Symbol	Description	Min.	Typ.	Max	Unit	Notes
t_{TX_CLK}	RGMII TX_CLK clock period		8		ns	125MHz to PHY
t_{OSU}	TXD & TX_EN setup time to rising edge of RGMII clock (no clock delay added)	1			ns	From PHY
	TXD & TX_EN setup time to rising edge of RGMII clock (clock delay added)	-0.9			ns	From PHY
t_{OHD}	RXD & DV hold time to rising edge of RGMII clock (no clock delay added)	0.8			ns	From PHY
	RXD & DV hold time to rising edge of RGMII clock (clock delay added)	2.7			ns	From PHY

Note: The RXD at t_{OHD} define is wrong description, checked with Amlogic and correct to TXD.

1.3 Test Result and picture

1.3.1 Management Data Timing

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tMDC	MDC clock Period	400	500	--	612	ns	Pass
tMDCSU	Setup time to rising edge of MDC	10	--	--	300	ns	Pass
tMDCHD	Hold time to rising edge of MDC	10	--	--	308	ns	Pass

Note: D4 is the longest trace, D0 is the shortest trace

1.3.2 RGMII Receive Timing

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tRX_CLK	RGMII RX_CLK clock period	--	8	--	8	ns	Pass
tISU	RXD[3:0] setup time	1.2	--	--	4.08	ns	Pass
tIHD	RXD[3:0] hold time	1.2	--	--	7.28	ns	Pass
tSKEW	RXD[3:0] skew between these 5 signal	-0.5	--	0.5	0.12	ns	Pass

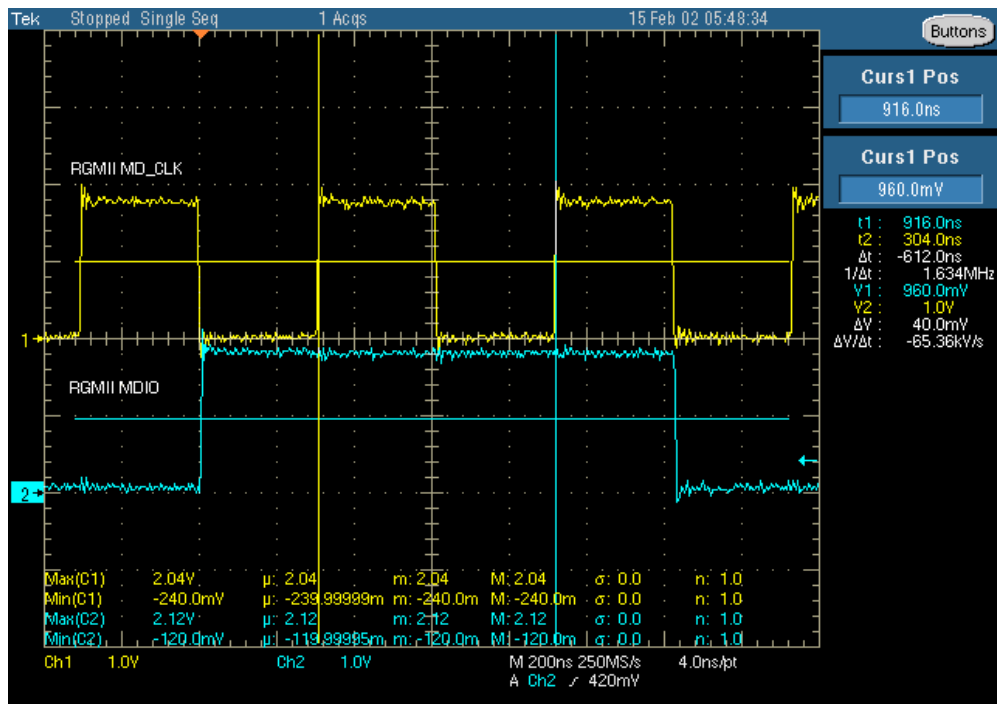
1.3.3 RGMII Transmit Timing

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tTX_CLK	RGMII TX_CLK clock period	--	8	--	8	ns	Pass
tOSU	TXD[3:0] setup time	1	--	--	5.48	ns	Pass
tOHD	TXD[3:0] hold time	0.8	--	--	6.12	ns	Pass

1.3.4 Picture of MDC timing

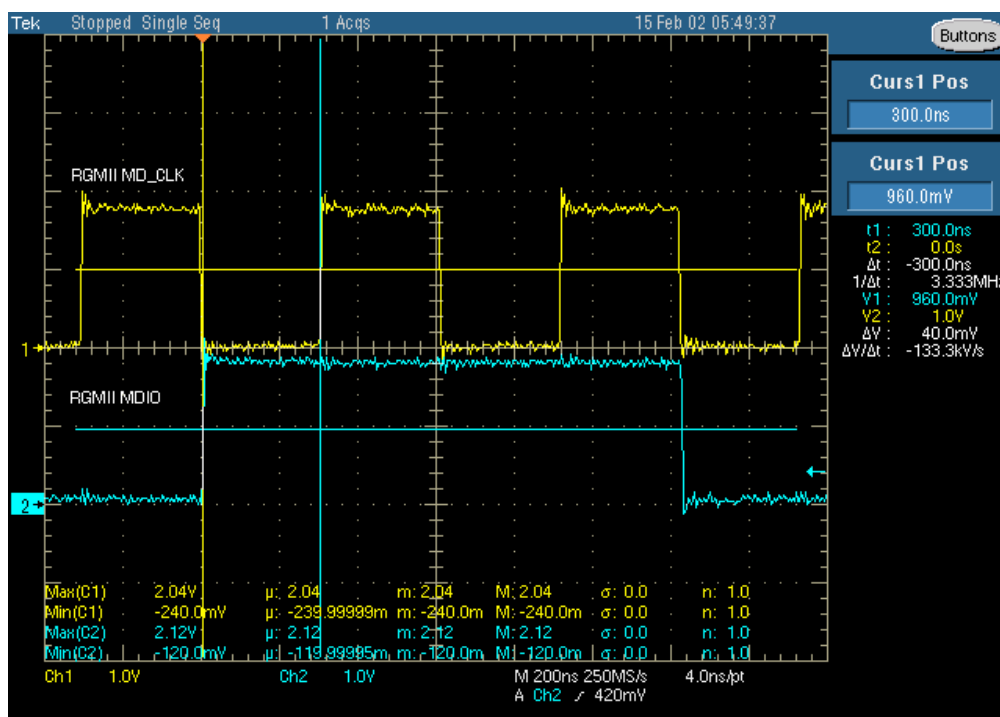
1.3.4.1 MDC - tMDC

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tMDC	MDC clock Period	400	500	--	612	ns	Pass

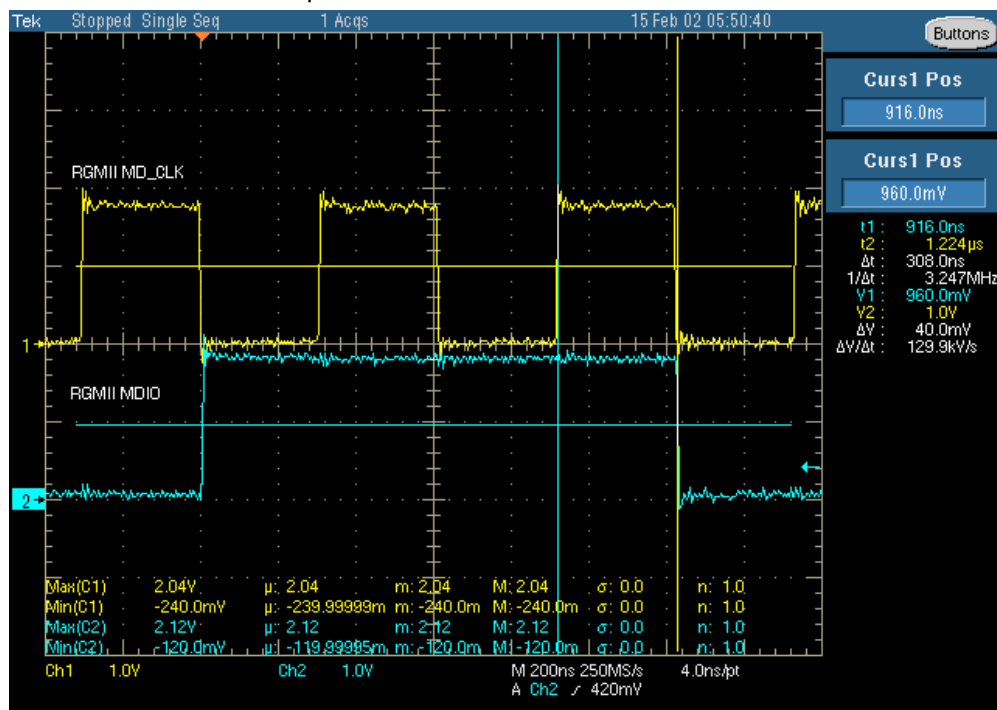


1.3.4.2 MDC - tMDCSU (Setup time to rising edge of MDC)

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tMDCSU	Setup time to rising edge of MDC	10	--	--	300	ns	Pass

**1.3.4.3 MDC - tMDCHD (Hold time to rising edge of MDC)**

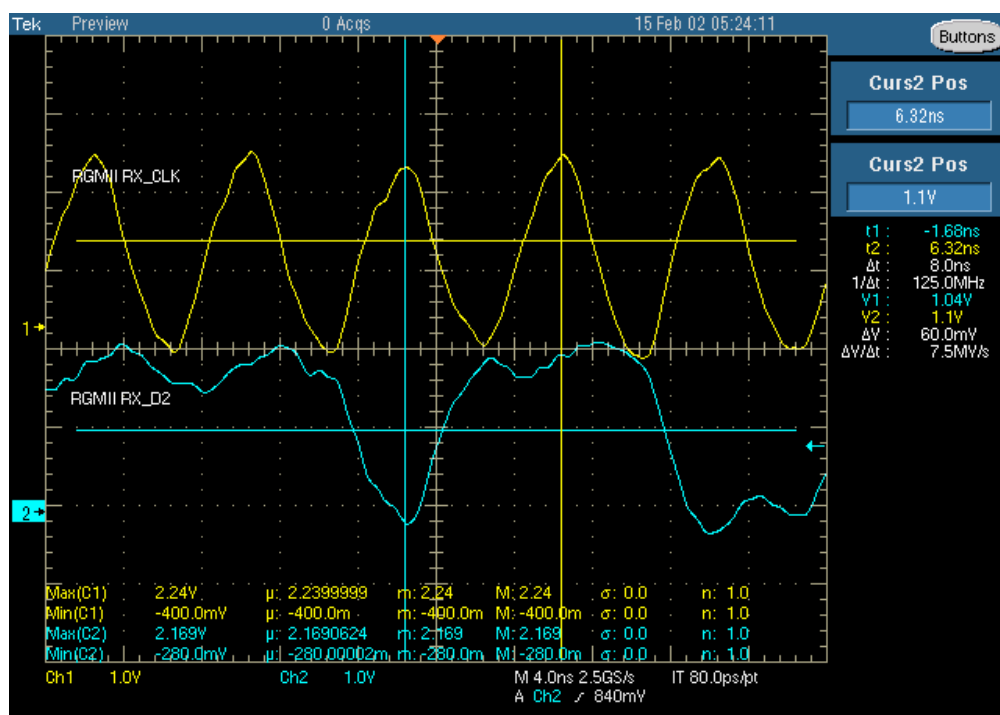
Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tMDCHD	Hold time to rising edge of MDC	10	--	--	308	ns	Pass



1.3.5 Picture of RGMII Receive Timing

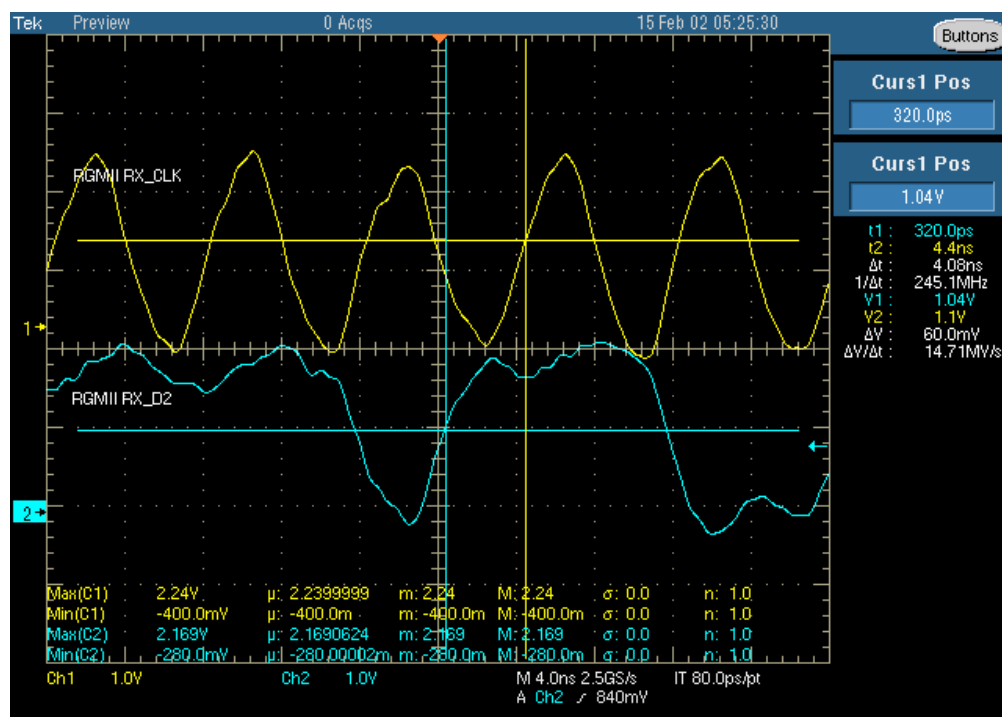
1.3.5.1 RGMII Receive Timing – RX clock (RGMII RX_CLK clock period)

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tRX_CLK	RGMII RX_CLK clock period	--	8	--	8	ns	Pass

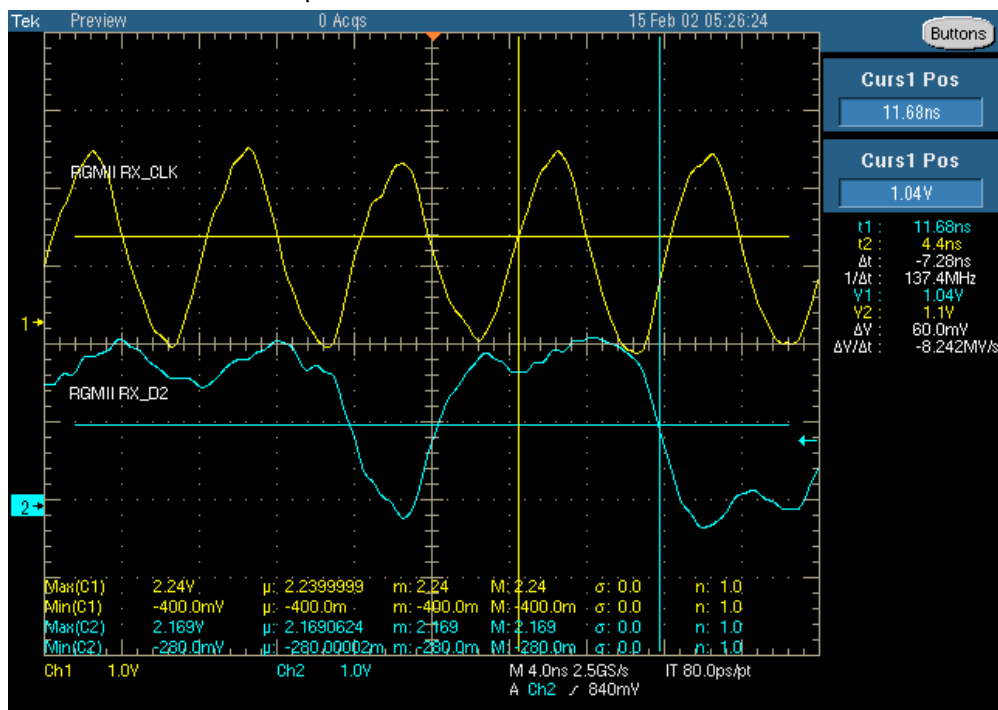


1.3.5.2 RGMII Receive Timing - tISU (RXD[3:0] setup time)

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tISU	RXD[3:0] setup time	1.2	--	--	4.08	ns	Pass

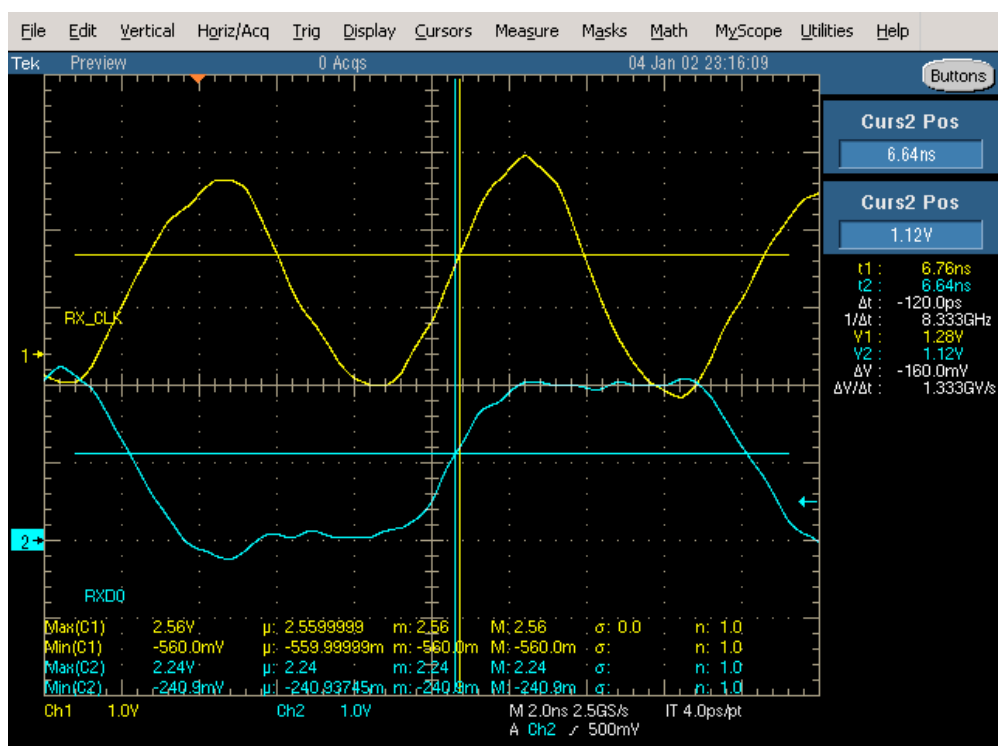
**1.3.5.3 RGMII Receive Timing - tIHD (RXD[3:0] hold time)**

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tIHD	RXD[3:0] hold time	1.2	--	--	7.28	ns	Pass



1.3.5.4 RGMII Receive Timing - tSKEW (RXD[3:0] skew between these 5 signal)

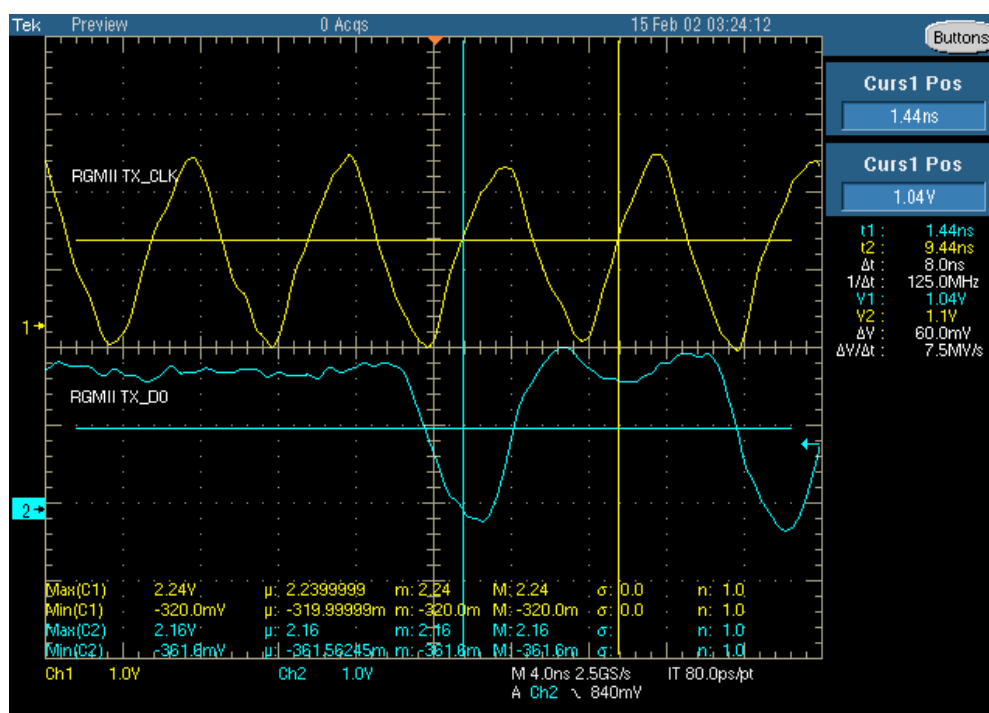
Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tSKEW	RXD[3:0] skew between these 5 signal	-0.5	--	0.5	0.12	ns	Pass



1.3.6 Picture of RGMII Transmit Timing

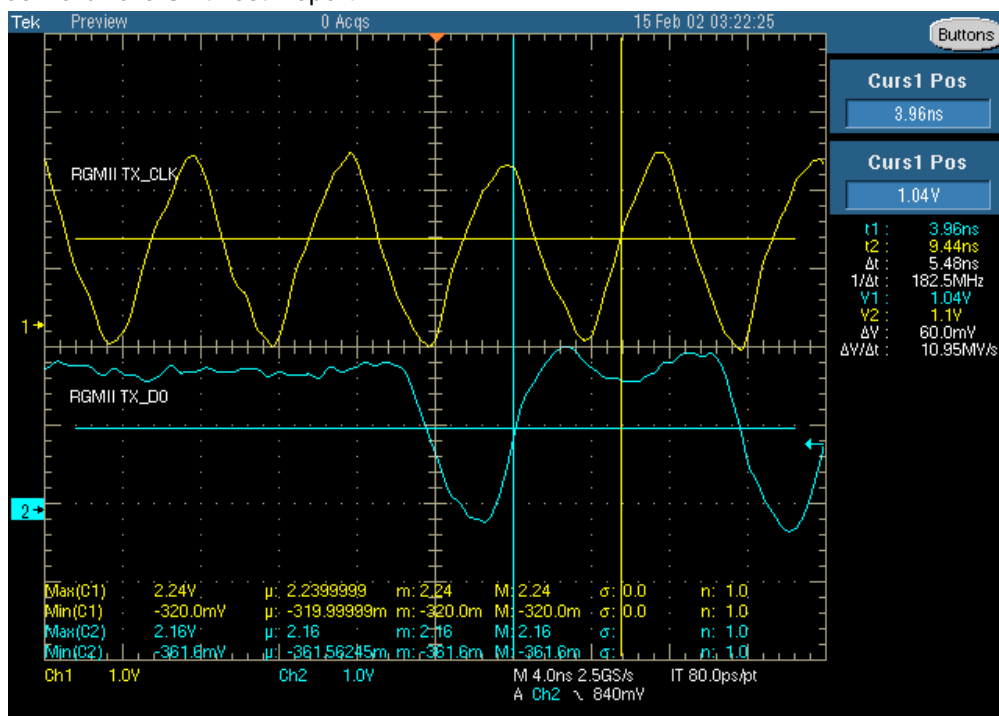
1.3.6.1 RGMII Transmit Timing – TX clock (RGMII TX_CLK clock period)

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tTX_CLK	RGMII TX_CLK clock period	--	8	--	8	ns	Pass



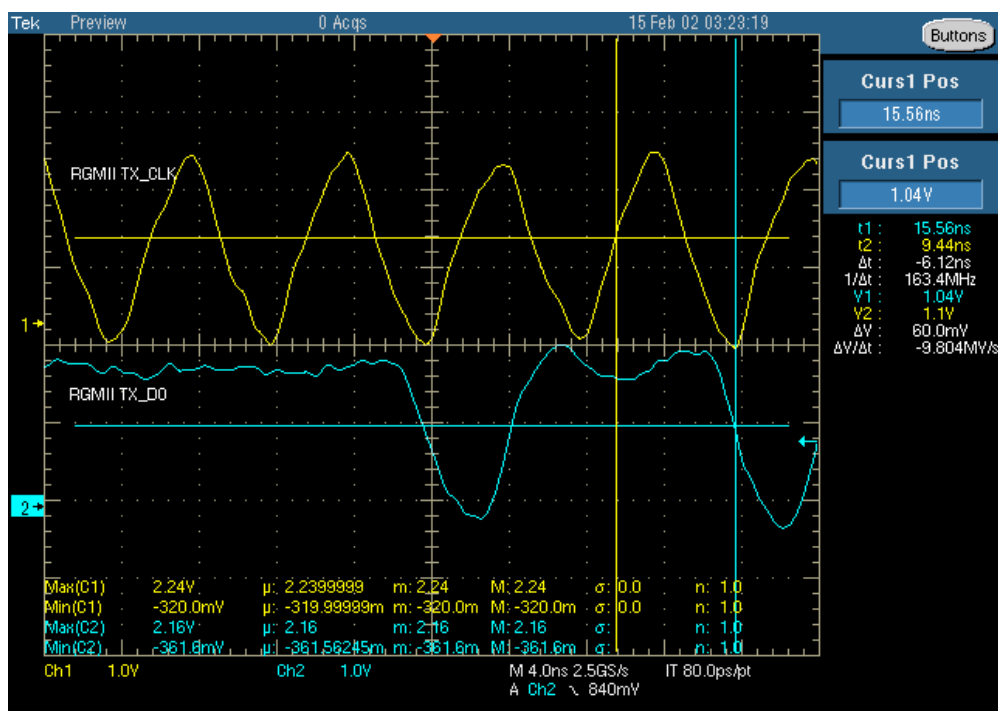
1.3.6.2 RGMII Transmit Timing - tOSU (TXD[3:0] setup time)

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tOSU	TXD[3:0] setup time	1	--	--	5.48	ns	Pass



1.3.6.3 RGMII Transmit Timing - tOHD (RXD[3:0] hold time)

Test Item		Specification			Result	Unit	Pass / Fail
Symbol	Parameter	Min.	Typ.	Max.			
tOHD	TXD[3:0] hold time	0.8	--	--	6.12	ns	Pass



1.4 LED status

1.4.1 LED function

Ethernet LED on RJ45 has 2 LED indicators, one is green LED and the other is orange LED.

Green LED: Link function, when Ethernet cable plugs onto RJ45, the Green LED blinks.

2 seconds is per cycle, every cycle blinks 1 second.

Orange LED: Active function, when RTL8211F gets data, the orange LED blinks irregularly.

Note: The LEDs do not define 10/100/1000 bps. Just any functional Ethernet cable plugged-in can work.

1.4.2 LED condition

Plugged-in Ethernet cable to RJ45 connector from Ethernet Hub and checks green LED blinks or not.

Check orange LED blinks irregularly after plugged-in Ethernet cable to RJ45 connector, the Ethernet hub will send broadcast network package to device and the orange LED will blink.

1.4.3 Test Result.



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So the test results are pass. Both LEDs can work normally.